

FIG. 1(A)

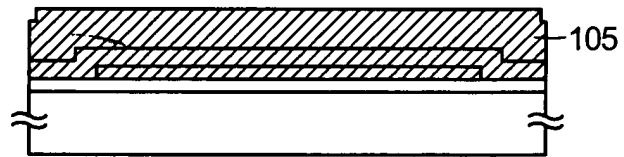


FIG. 1(B)

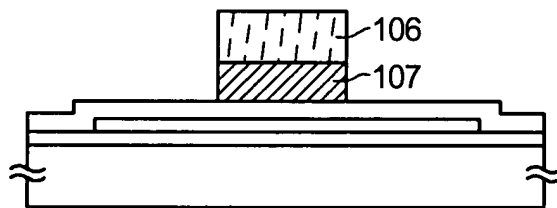


FIG. 1(C)

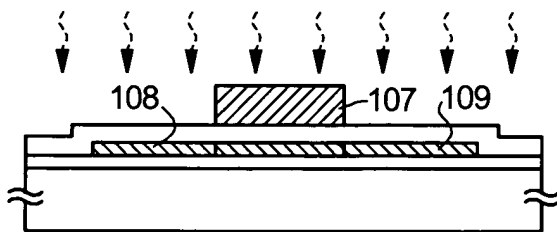


FIG. 1(D)

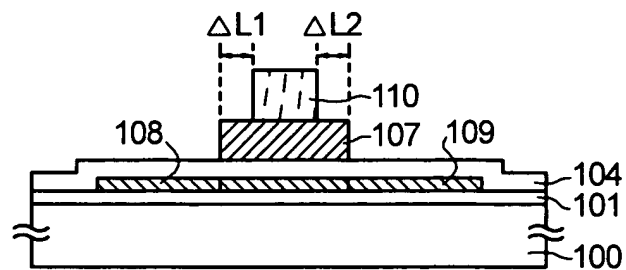


FIG. 2(A)

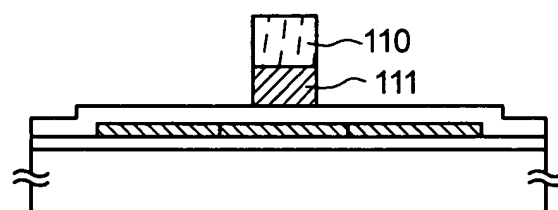


FIG. 2(B)

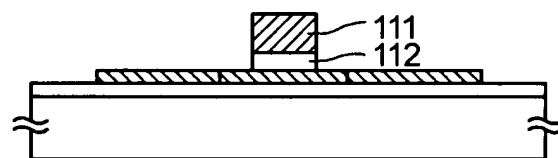


FIG. 2(C)

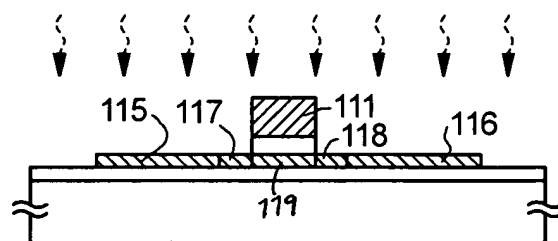


FIG. 2(D)

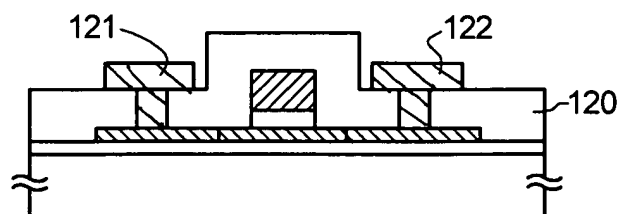


FIG. 2(E)



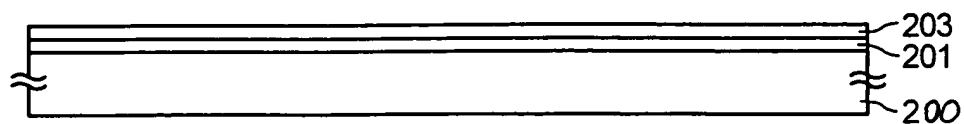


FIG. 3(A)

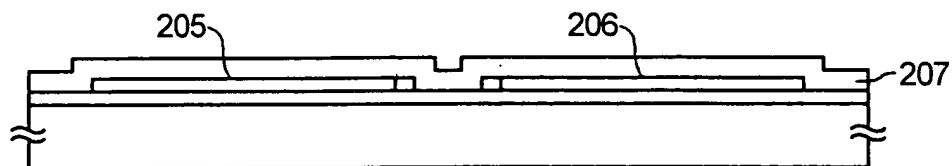


FIG. 3(B)

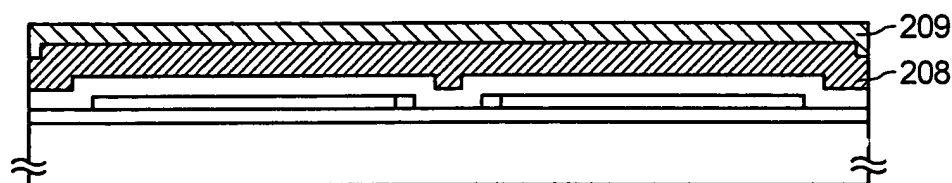


FIG. 3(C)

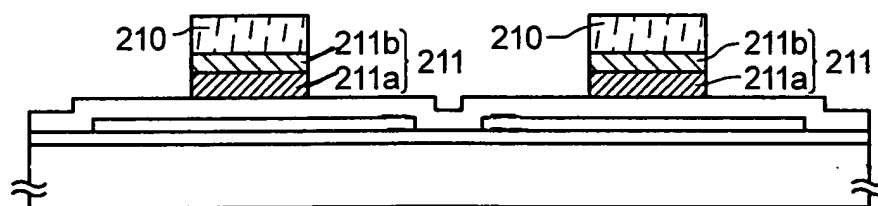


FIG. 3(D)

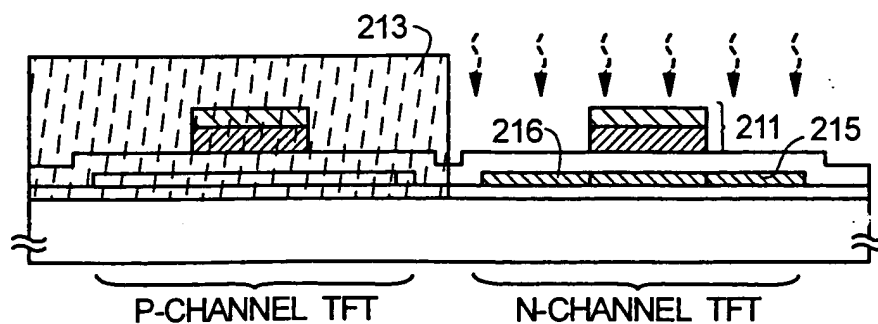


FIG. 3(E)



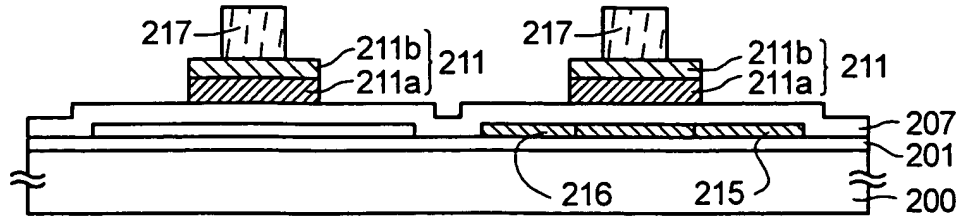


FIG. 4(A)

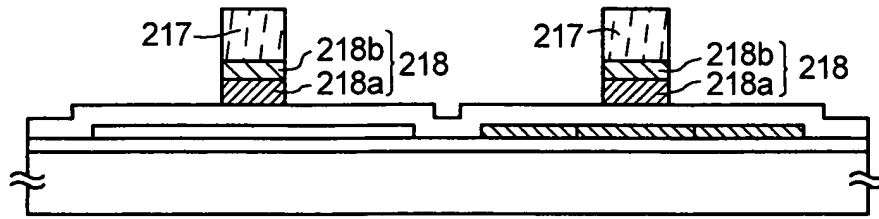


FIG. 4(B)

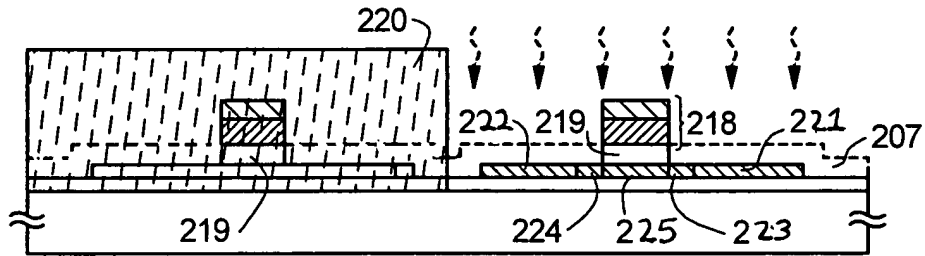


FIG. 4(C)

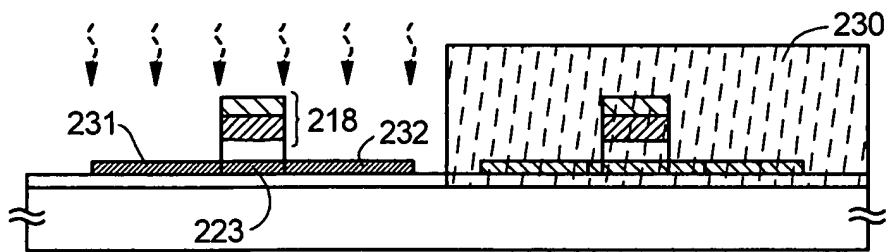


FIG. 4(D)

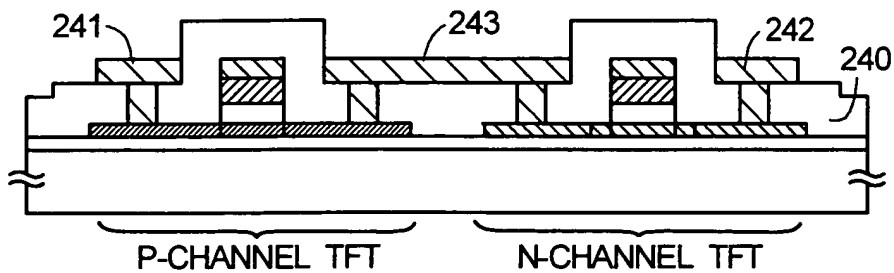
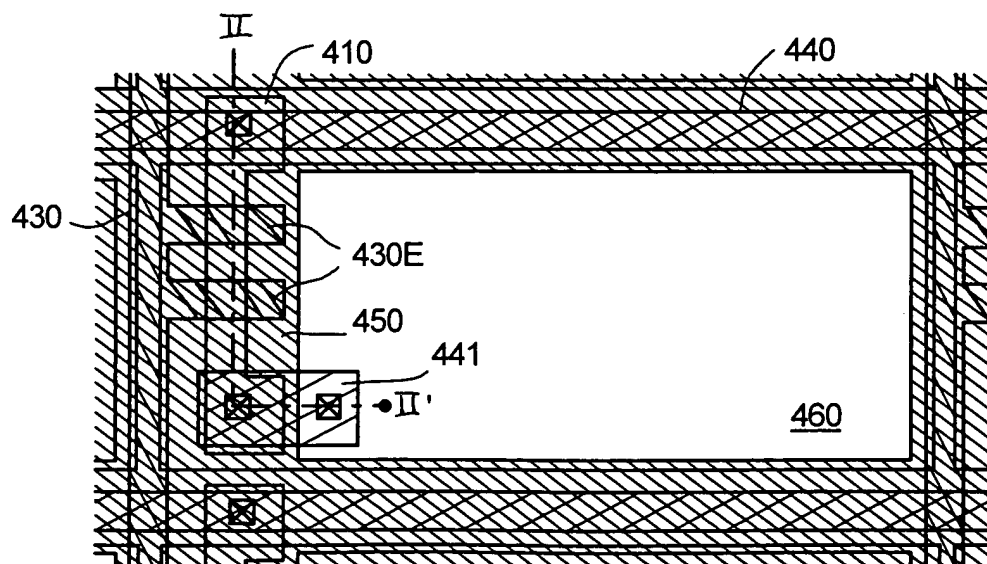
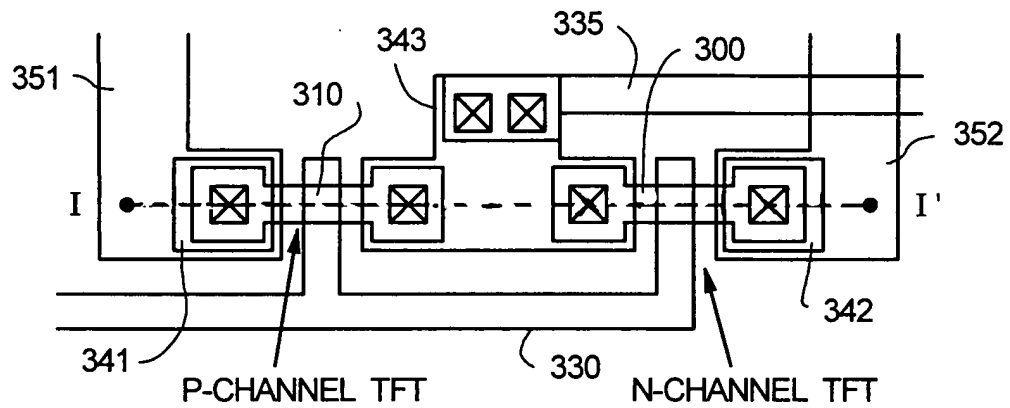
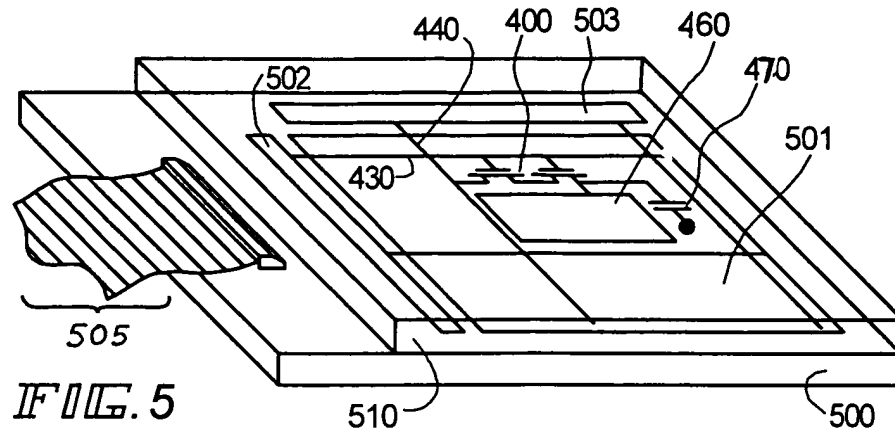
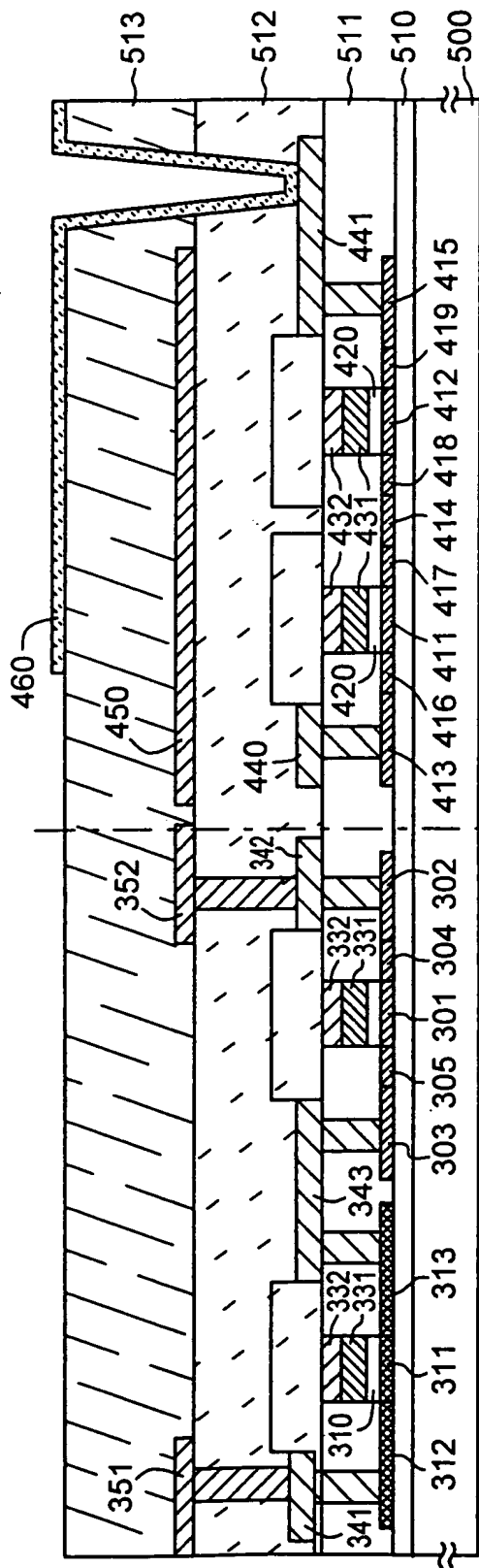


FIG. 4(E)





P CHANNEL TFT N CHANNEL TFT

PIXEL TFT (N-CHANNEL) 400

CMOS CIRCUIT
CROSS SECTION AT I - I'

PIXEL MATRIX CIRCUIT 501
CROSS SECTION AT II - II'

FIG. 7

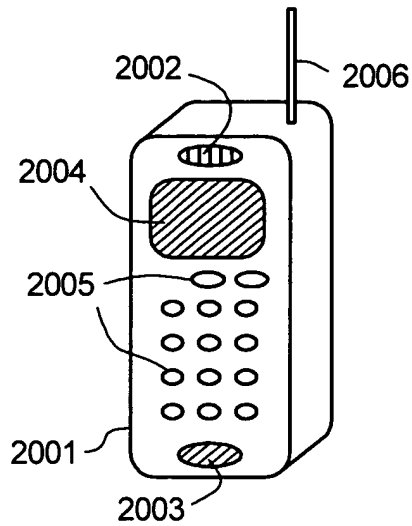


FIG. 8(A)

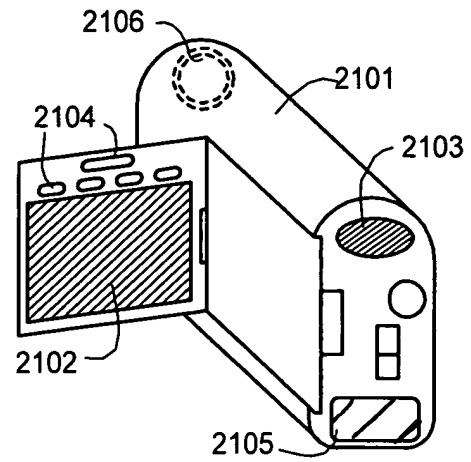


FIG. 8(B)

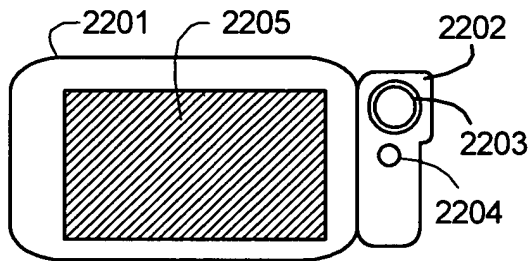


FIG. 8(C)

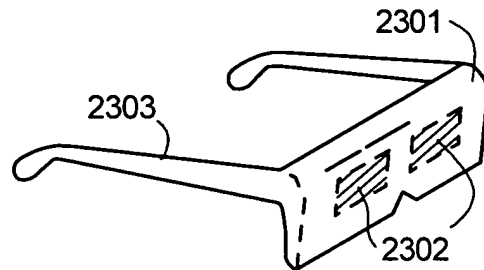


FIG. 8(D)

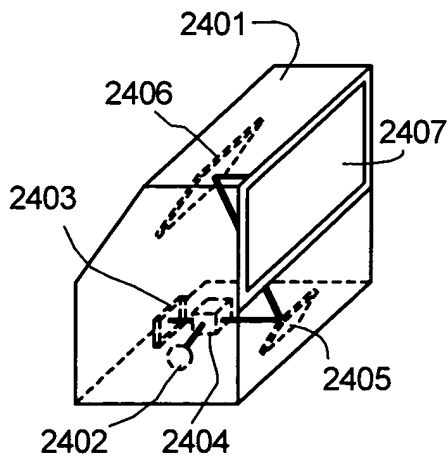


FIG. 8(E)

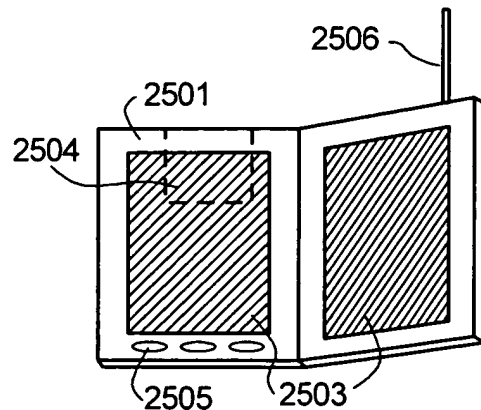


FIG. 8(F)





FIG. 9(A)
PRIOR ART

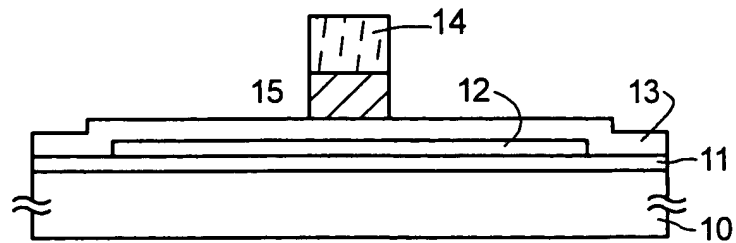


FIG. 9(B)
PRIOR ART

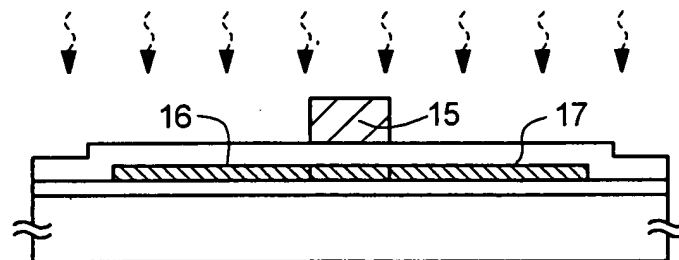


FIG. 9(C)
PRIOR ART

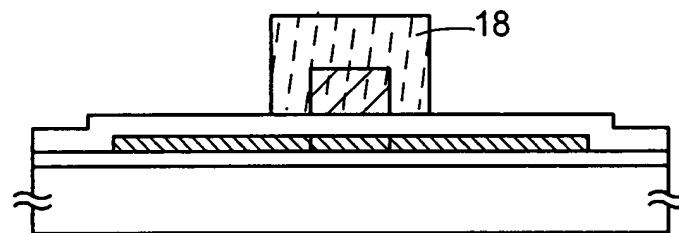


FIG. 9(D)
PRIOR ART

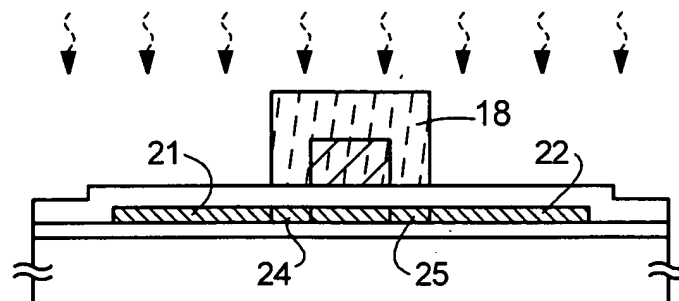
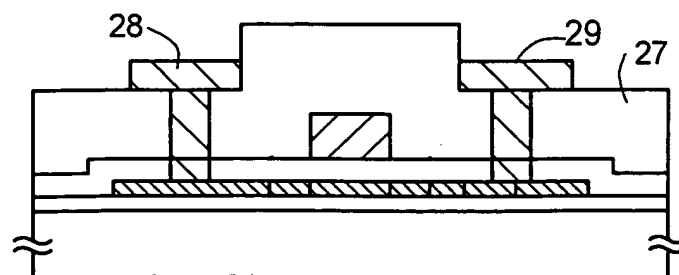
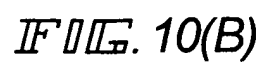
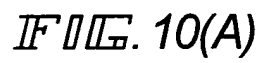


FIG. 9(E)
PRIOR ART





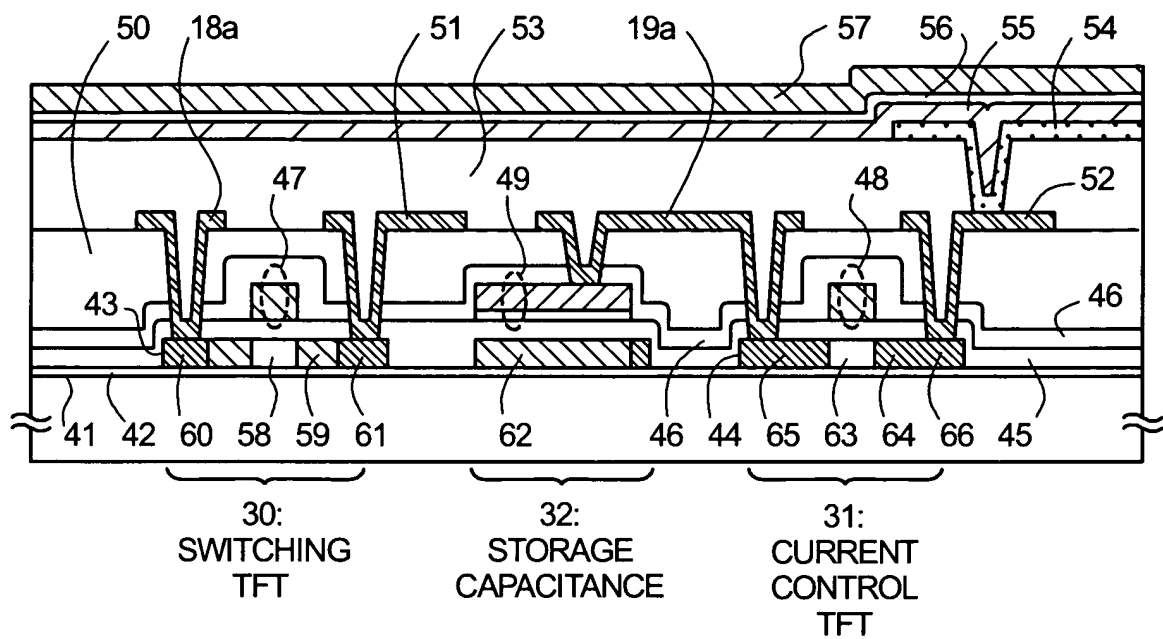


FIG. 11

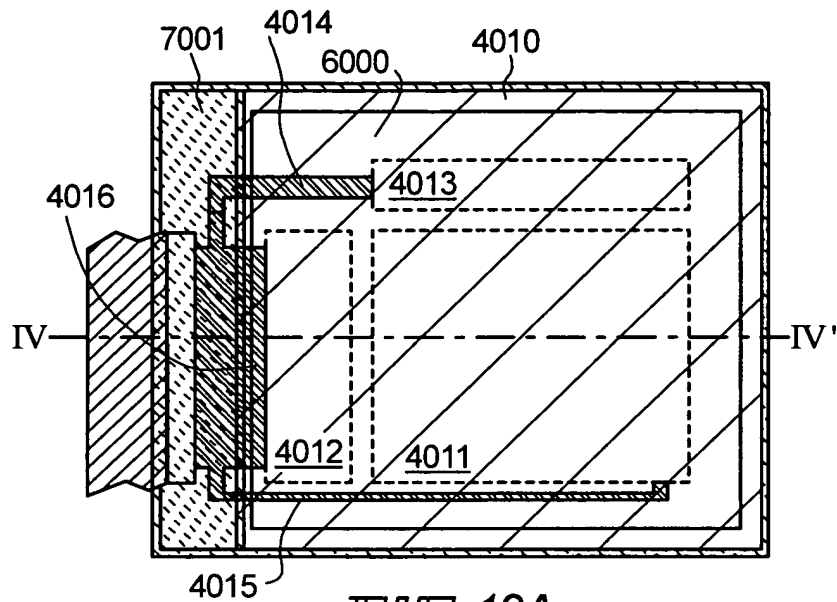


FIG. 12A

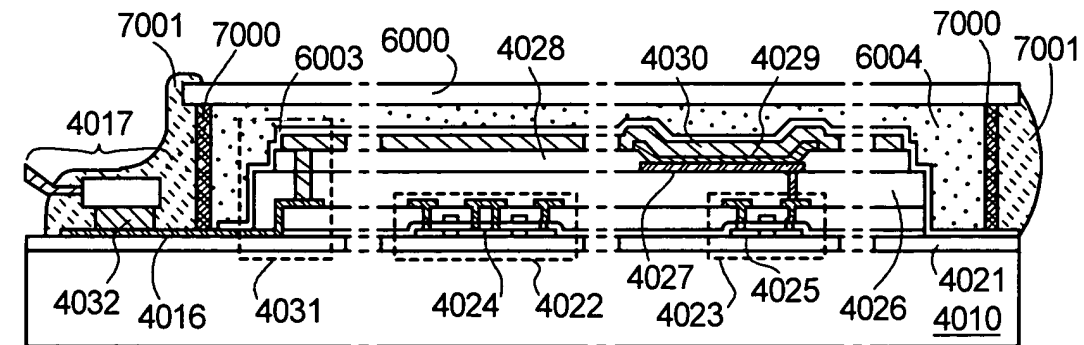


FIG. 12B

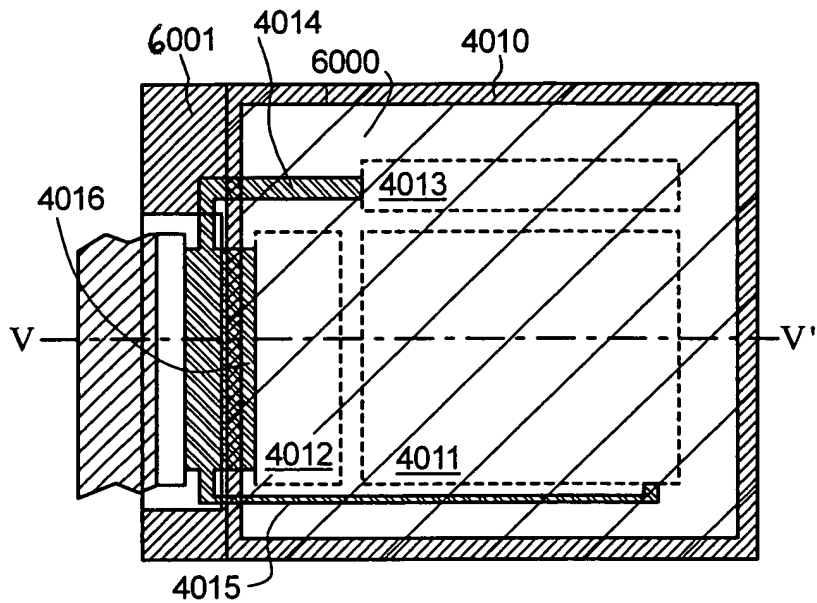


FIG. 13A

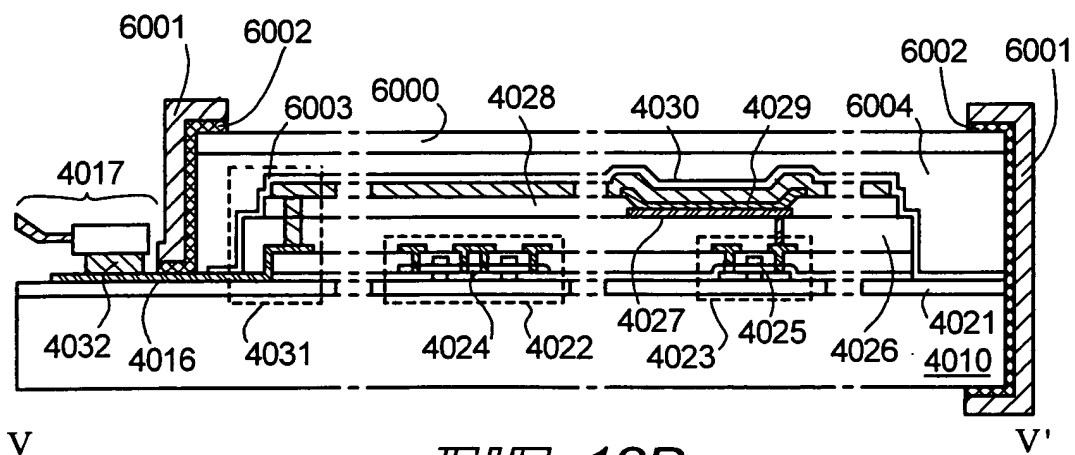
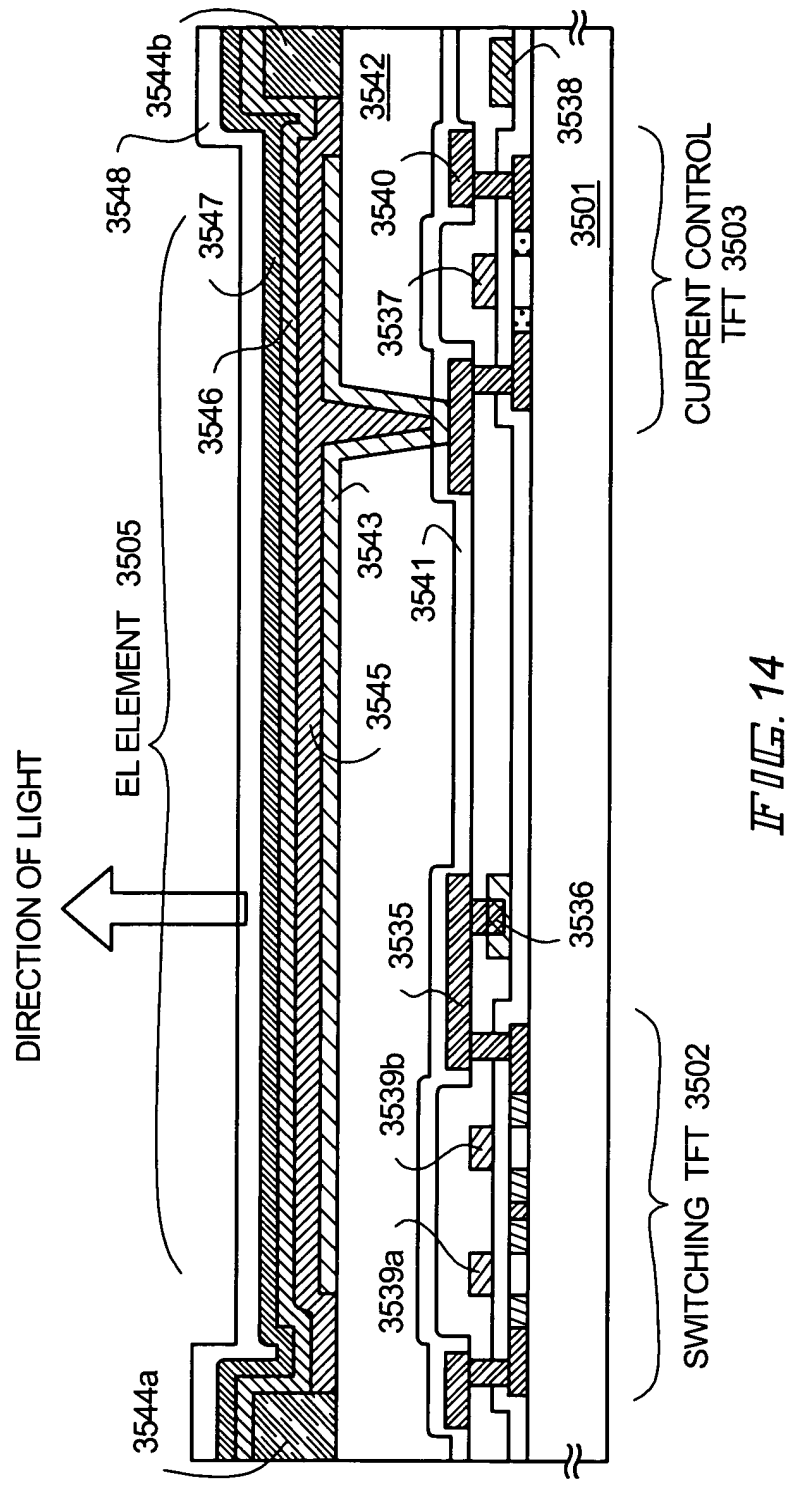


FIG. 13B



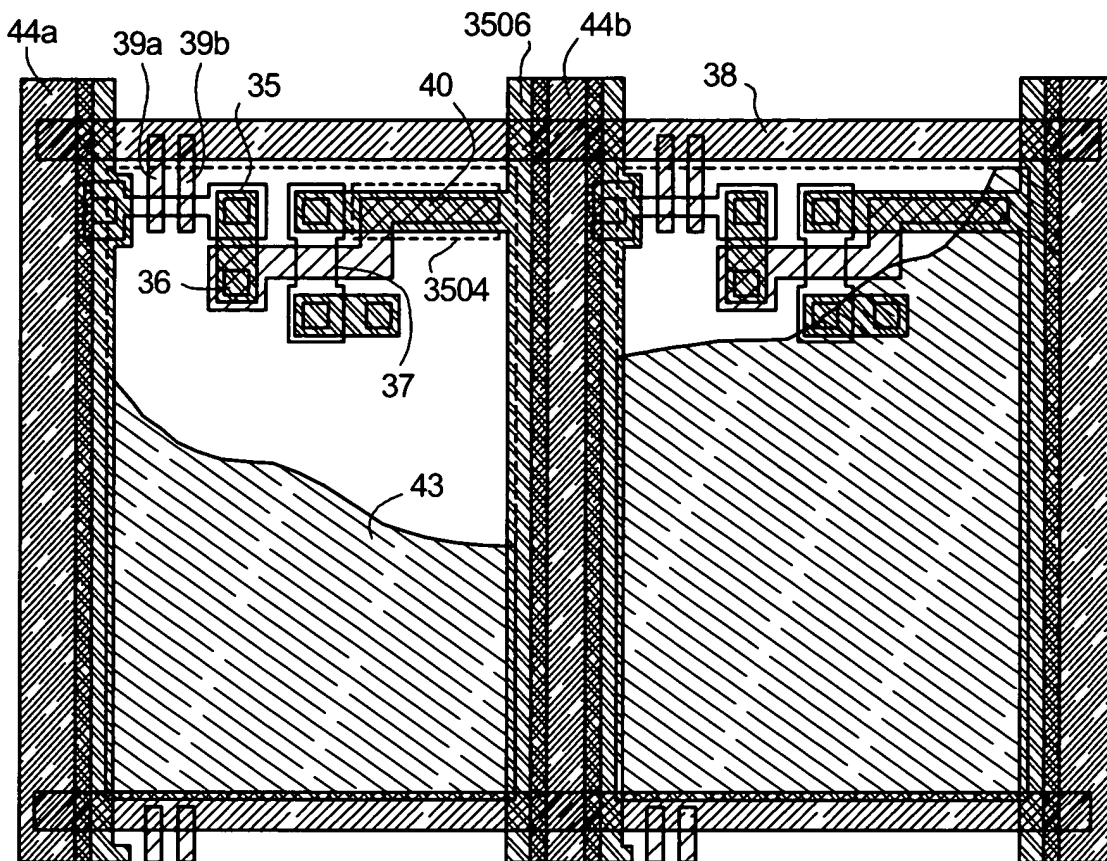


FIG. 15A

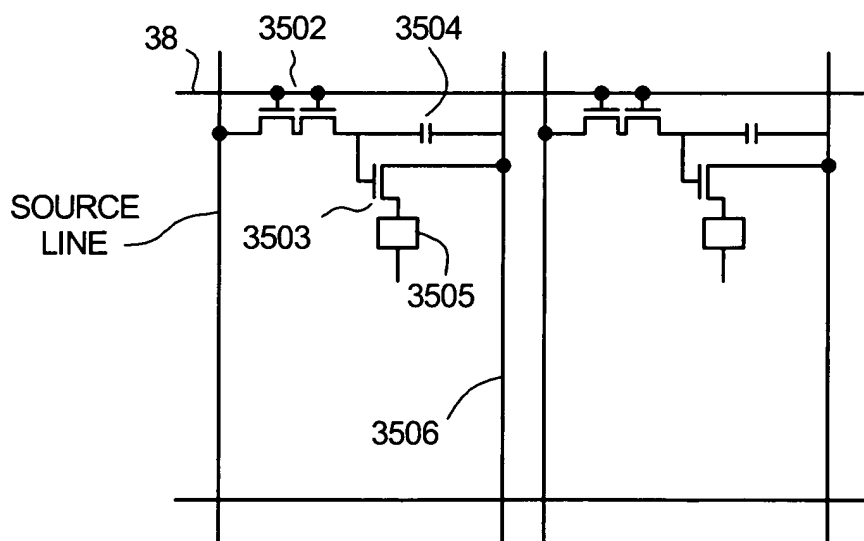


FIG. 15B

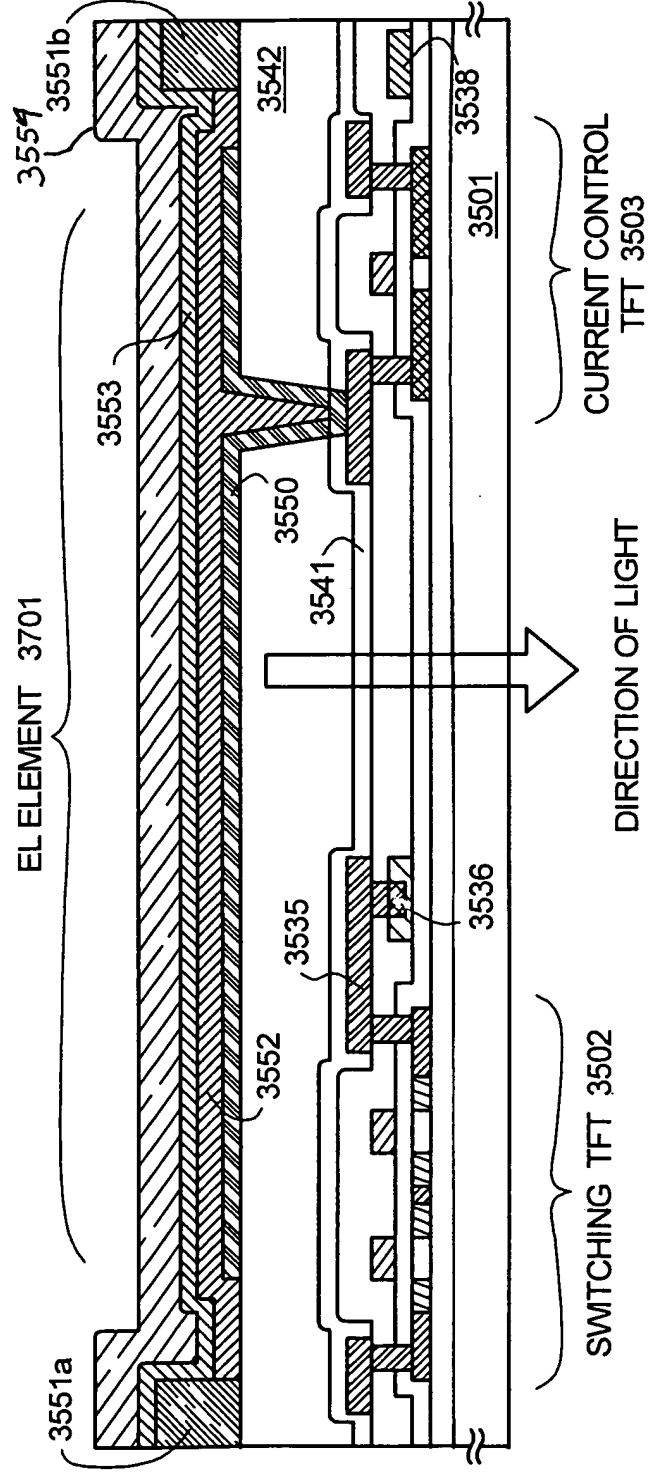


FIG. 16



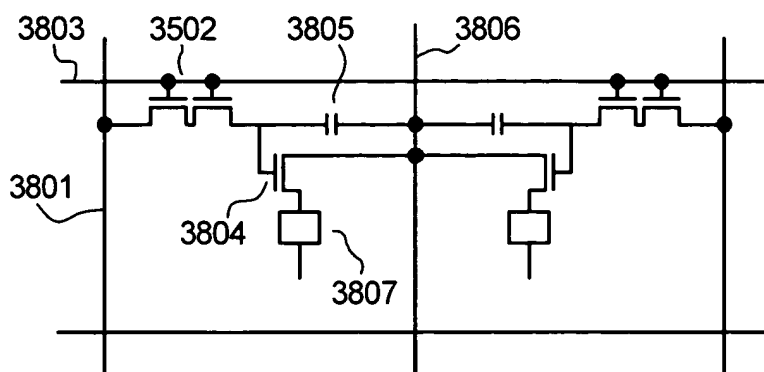


FIG. 17A

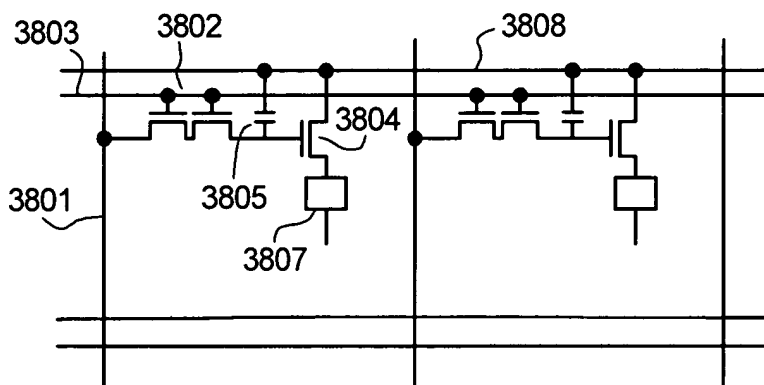


FIG. 17B

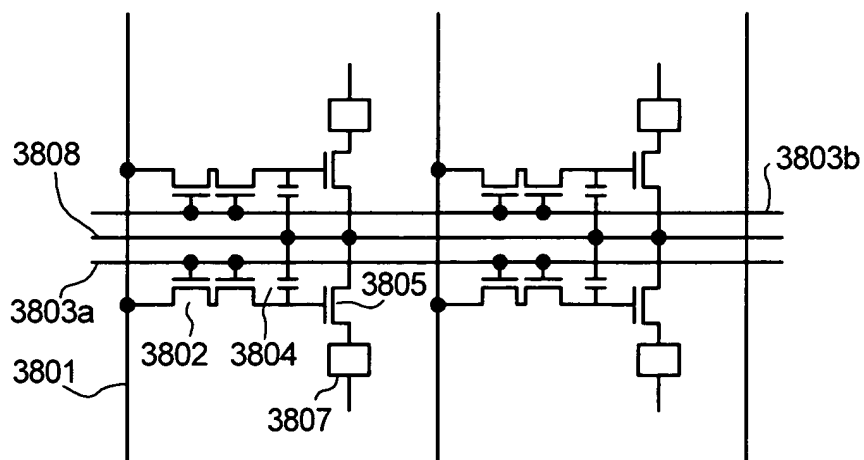


FIG. 17C

